

FIG. 1

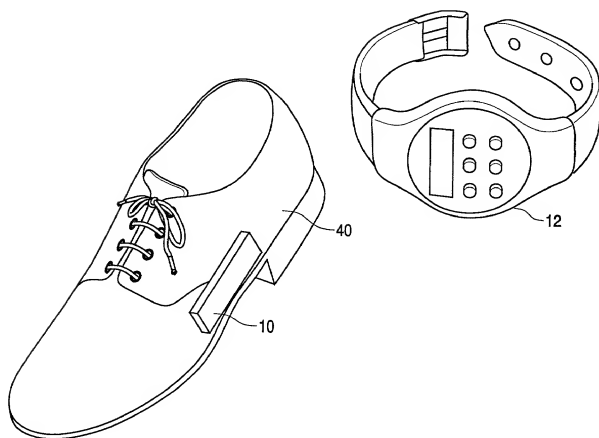


FIG. 4

RUN-IN	FRAMING	DATABLOCK 1	DATABLOCK 2	DATABLOCK 3	FLAG A	FLAG B	FLAG C
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FIG.2

	Nibble 1		Nibble 2		Nibble 3		Flag A
Message 1	FSR 3-0 XOR Address 3-0		Fn ; FSR 5-4		Address 3-0		M = 1
Message 2	Address 7-4		**Distance 7-4		**Distance 3-0		
Message 3	Address 11-8		**Distance 15-12		**Distance 11-8		(V low)
Message 4	Address 15-12		Spare		Volts / Spare		

FIG.3A

	Nibble 1	Nibble 2	Nibble 3	Flag A
Message 1 a	FSR 3-0 XOR Address 3-0	Speed 7-4	Speed 3-0	M = 0
Message 2 a	F _n = 01 ; FSR 5-4 (message 1a)	Speed 7-4	Speed 3-0	(RW)
Message 3 a	Address 7-4	Speed 7-4	Speed 3-0	(V low)
Message 4 a	**Distance 7-4	Speed 7-4	Speed 3-0	
Message 1 b	FSR 3-0 XOR Address 3-0	Speed 7-4	Speed 3-0	M = 0
Message 2 b	F _n = 10 ; FSR 5-4 (message 1b)	Speed 7-4	Speed 3-0	(RW)
Message 3 b	Address 11-8	Speed 7-4	Speed 3-0	(V low)
Message 4 b	**Distance 11-8	Speed 7-4	Speed 3-0	
Message 1 c	FSR 3-0 XOR Address 3-0	Speed 7-4	Speed 3-0	M = 0
Message 2 c	F _n = 11 ; FSR 5-4 (message 1c)	Speed 7-4	Speed 3-0	(RW)
Message 3 c	Address 15-12	Speed 7-4	Speed 3-0	(V low)
Message 4 c	**Distance 15-12	Speed 7-4	Speed 3-0	

FIG. 3B